

M87x Family, Modular Monitoring and Recording IEDs



M871 IED in 7 Bay Chassis



M872 IED in 12 Bay Chassis

Model Selection Guide:

M87x Family of Modular Monitoring and Recording IEDs							
Line 1		Line 2		Aux	Model	Modular Chassis	Page
Voltage Input	Current Input	Voltage Input	Current Input	Voltage Input			
A, B, C, N	A, B, C, N	A, B, C, N	none	Aux1, Aux2		7 Bay	6
					M871	10 Bay	8
					M871	12 Bay	10
A, B, C, N	A, B, C	A, B, C, N	A, B, C	none	M872	10 Bay	12
					M872	12 Bay	14

M87x Family Order Guides

Model: M871

M871, Modular IED in Small Chassis

Category and Description of Choices (X is Default)

Base Model

M871, IED, Modular, Single Line, Dual Bus, C07A5 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac

Bay2, cPCI Slot

C00, Blank Cover
 P30A, Digital I/O, 8 point set for 100 V dc
 P30A, Digital I/O, 8 point set for 300 V dc
 P33, Eight Digital Outputs
 P40, Transducer Module, 8 inputs set for 0-10 V dc
 P40, Transducer Module, 8 inputs set for 4-20 mA
 P40, Transducer Module, 8 inputs set for 0-1 mA
 None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover
 P30A, Digital I/O, 8 point set for 100 V dc
 P31, Digital I/O, 16 point set for 100 V dc
 P30A, Digital I/O, 8 point set for 300 V dc
 P31, Digital I/O, 16 point set for 300 V dc
 P33, Eight Digital Outputs
 P40, Transducer Module, 8 inputs set for 0-10 V dc
 P40, Transducer Module, 8 inputs set for 4-20 mA
 P40, Transducer Module, 8 inputs set for 0-1 mA
 None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover
 P30A, Digital I/O, 8 point set for 100 V dc
 P31, Digital I/O, 16 point set for 100 V dc
 P30A, Digital I/O, 8 point set for 300 V dc
 P31, Digital I/O, 16 point set for 300 V dc
 P33, Eight Digital Outputs
 P40, Transducer Module, 8 inputs set for 0-10 V dc
 P40, Transducer Module, 8 inputs set for 4-20 mA
 P40, Transducer Module, 8 inputs set for 0-1 mA
 Open for H12 with Ethernet option selected in next slot

Bay5, cPCI Slot, Host

H12M256, Host Processor, 256 MB
 H12M512, Host Processor, 512 MB
 H12M1GB, Host Processor, 1 GB
 H12M2GB, Host Processor, 2 GB
 H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)
 H12M512E1, Host Processor, 512 MB, with E1 Ethernet (RJ45 10/100Mb)
 H12M1GBE1, Host Processor, 1 GB, with E1 Ethernet (RJ45 10/100Mb)
 H12M2GBE1, Host Processor, 2 GB, with E1 Ethernet (RJ45 10/100Mb)
 H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
 H12M512E3, Host Processor, 512 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
 H12M1GBE3, Host Processor, 1 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
 H12M2GBE3, Host Processor, 2 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay6, cPCI Slot, DSP

A10, Analog Processor

Build Order#

Position 1-4

M871

Position 5

2

Position 6

0
6
8
A
U
V
W
Y

Position 7

0
6
7
8
9
A
U
V
W
Y

Position 8

0
6
7
8
9
A
U
V
W
Y

Position 9

8
9
A
H
B
C
D
J
E
F
G
K

Position 10

1

Order Guide continues on next page

M87x Family Order Guides

Bay7, Signal Input

- S10, 5 A Nominal, Linear to 100 Amp, UL/CSA
- S11, 5 A Nominal, Linear to 20 Amp, UL/CSA
- S12, 1 A Nominal, Linear to 4 Amp, UL/CSA
- S10, 5 A Nominal, Linear to 100 Amp, CE
- S11, 5 A Nominal, Linear to 20 Amp, CE
- S12, 1 A Nominal, Linear to 4 Amp, CE
- S1C, 5A nominal, external split-core CTs (3), linear to 100 Amp – No UL, CE & CSA
- S1C, 5A nominal, external split-core CTs (4), linear to 100 Amp – No UL, CE & CSA

Firmware Level

X, Latest Firmware Version

Position 11

- 1
- 2
- 3
- 4
- 5
- 6
- T
- Q

Position 12

C

End of Order Guide

M87x Family Order Guides

Model: **M871**

M871, Modular IED in Intermediate Chassis

Category and Description of Choices (X is Default)

Base Model

M871, IED, Modular, Single Line, Dual Bus, C10A7 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac

Bay2, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay5, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay6, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

Open for H12 with Ethernet option selected in next slot

Build Order#

Position 1-4

M871

Position 5

2

Position 6

0

6

8

A

U

V

W

Y

Position 7

0

6

7

8

9

A

U

V

W

Y

Position 8

0

6

7

8

9

A

U

V

W

Y

Position 9

0

6

7

8

9

A

U

V

W

Y

Position 10

0

6

7

8

9

A

Y

Order Guide continues on next page



M87x Family Order Guides

Model: **M871**

M871, Modular IED in Intermediate Chassis

Category and Description of Choices (X is Default)

Bay7, cPCI Slot, Host

H12M256, Host Processor, 256 MB
H12M512, Host Processor, 512 MB
H12M1GB, Host Processor, 1 GB
H12M2GB, Host Processor, 2 GB
H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M512E1, Host Processor, 512 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M1GBE1, Host Processor, 1 GB, with E1 Ethernet (RJ45 10/100Mb)
H12M2GBE1, Host Processor, 2 GB, with E1 Ethernet (RJ45 10/100Mb)
H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M512E3, Host Processor, 512 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M1GBE3, Host Processor, 1 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M2GBE3, Host Processor, 2 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay8, cPCI Slot, DSP

A10, Analog Processor

Bay9

C00, Blank Cover

Bay10, Signal Input

S10, 5 A Nominal, Linear to 100 Amp, UL/CSA
S11, 5 A Nominal, Linear to 20 Amp, UL/CSA
S12, 1 A Nominal, Linear to 4 Amp, UL/CSA
S10, 5 A Nominal, Linear to 100 Amp, CE
S11, 5 A Nominal, Linear to 20 Amp, CE
S12, 1 A Nominal, Linear to 4 Amp, CE
S1C, 5A nominal, external split-core CTs (3), linear to 100 Amp – No UL, CE & CSA
S1C, 5A nominal, external split-core CTs (4), linear to 100 Amp – No UL, CE & CSA

Firmware Level

X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 11

8
9
A
H
B
C
D
J
E
F
G
K

Position 12

1

Position 13

0

Position 14

1
2
3
4
5
6
T
Q

Position 15

C

M87x Family Order Guides

Model: **M871**

M871, Modular IED in Large Chassis

Category and Description of Choices (X is Default)

Base Model

M871, IED, Modular, Single Line, Dual Bus, C12A8 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac plus blank slot cover

V10NC, 20-300 V dc/55-285 V ac, cover by P31 next Slot

Bay2, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay5, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Build Order#

Position 1-3

871

Position 4

3

4

Position 5

0

6

7

8

9

A

U

V

W

Y

Position 6

0

6

7

8

9

A

U

V

W

Y

Position 7

0

6

7

8

9

A

U

V

W

Y

Position 8

0

6

7

8

9

A

U

V

W

Y

Order Guide continues on next page



M87x Family Order Guides

Model: **M871**

M871, Modular IED in Large Chassis

Category and Description of Choices (X is Default)

Bay6, cPCI Slot

- C00, Blank Cover
- P30A, Digital I/O, 8 point set for 100 V dc
- P31, Digital I/O, 16 point set for 100 V dc
- P30A, Digital I/O, 8 point set for 300 V dc
- P31, Digital I/O, 16 point set for 300 V dc
- P33, Eight Digital Outputs
- P40, Transducer Module, 8 inputs set for 0-10 V
- P40, Transducer Module, 8 inputs set for 4-20 mA
- P40, Transducer Module, 8 inputs set for 0-1 mA
- None, covered by P31 in next slot

Bay7, cPCI Slot

- C00, Blank Cover
- P30A, Digital I/O, 8 point set for 100 V dc
- P31, Digital I/O, 16 point set for 100 V dc
- P30A, Digital I/O, 8 point set for 300 V dc
- P31, Digital I/O, 16 point set for 300 V dc
- P33, Eight Digital Outputs
- Open for H12 with Ethernet option selected in next slot

Bay8, cPCI, Slot Host

- H12M256, Host Processor, 256 MB
- H12M512, Host Processor, 512 MB
- H12M1GB, Host Processor, 1 GB
- H12M2GB, Host Processor, 2 GB
- H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)
- H12M512E1, Host Processor, 512 MB, with E1 Ethernet (RJ45 10/100Mb)
- H12M1GBE1, Host Processor, 1 GB, with E1 Ethernet (RJ45 10/100Mb)
- H12M2GBE1, Host Processor, 2 GB, with E1 Ethernet (RJ45 10/100Mb)
- H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
- H12M512E3, Host Processor, 512 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
- H12M1GBE3, Host Processor, 1 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
- H12M2GBE3, Host Processor, 2 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay9, cPCI Slot, DSP

- A10, Analog Processor

Bay10

- C00, Blank Cover

Bay11, Signal Input

- S10, 5 A Nominal, Linear to 100 Amp, UL/CSA
- S11, 5 A Nominal, Linear to 20 Amp, UL/CSA
- S12, 1 A Nominal, Linear to 4 Amp, UL/CSA
- S10, 5 A Nominal, Linear to 100 Amp, CE
- S11, 5 A Nominal, Linear to 20 Amp, CE
- S12, 1 A Nominal, Linear to 4 Amp, CE
- S1C, 5A nominal, external split-core CTs (3), linear to 100 Amp – No UL, CE & CSA
- S1C, 5A nominal, external split-core CTs (4), linear to 100 Amp – No UL, CE & CSA

Firmware Level

- X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 9

- 0
- 6
- 7
- 8
- 9
- A
- U
- V
- W
- Y

Position 10

- 0
- 6
- 7
- 8
- 9
- A
- Y

Position 11

- 8
- 9
- A
- H
- B
- C
- D
- J
- E
- F
- G
- K

Position 12

- 1

Position 13

- 0

Position 14

- 1
- 2
- 3
- 4
- 5
- 6
- T
- Q

Position 15

- C

M87x Family Order Guides

Model: **M872**

M872, Modular IED in Intermediate Chassis

Category and Description of Choices (X is Default)

Base Model

M872, IED, Modular, Dual Line, Dual Bus, C10A7 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac

Bay2, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay5, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

P40, Transducer Module, 8 inputs set for 0-10 V dc

P40, Transducer Module, 8 inputs set for 4-20 mA

P40, Transducer Module, 8 inputs set for 0-1 mA

None, covered by P31 in next slot

Bay6, cPCI Slot

C00, Blank Cover

P30A, Digital I/O, 8 point set for 100 V dc

P31, Digital I/O, 16 point set for 100 V dc

P30A, Digital I/O, 8 point set for 300 V dc

P31, Digital I/O, 16 point set for 300 V dc

P33, Eight Digital Outputs

Open for H12 with Ethernet option selected in next slot

Build Order#

Position 1-4

M872

Position 5

2

Position 6

0

6

8

A

U

V

W

Y

Position 7

0

6

7

8

9

A

U

V

W

Y

Position 8

0

6

7

8

9

A

U

V

W

Y

Position 9

0

6

7

8

9

A

U

V

W

Y

Position 10

0

6

7

8

9

A

Y

Order Guide continues on next page



M87x Family Order Guides

Model: **M872**

M872, Modular IED in Intermediate Chassis

Category and Description of Choices (X is Default)

Bay7, cPCI Slot, Host

H12M256, Host Processor, 256 MB
H12M512, Host Processor, 512 MB
H12M1GB, Host Processor, 1 GB
H12M2GB, Host Processor, 2 GB
H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M512E1, Host Processor, 512 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M1GBE1, Host Processor, 1 GB, with E1 Ethernet (RJ45 10/100Mb)
H12M2GBE1, Host Processor, 2 GB, with E1 Ethernet (RJ45 10/100Mb)
H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M512E3, Host Processor, 512 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M1GBE3, Host Processor, 1 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M2GBE3, Host Processor, 2 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay8, cPCI Slot, DSP

A10, Analog Processor

Bay9

None, Bay is covered by adjacent Signal Input Module

Bay10, Signal Input

S13, 5 A Nominal, Linear to 100 Amp, UL/CSA
S14, 5 A Nominal, Linear to 20 Amp, UL/CSA
S15, 1 A Nominal, Linear to 4 Amp, UL/CSA
S13, 5 A Nominal, Linear to 100 Amp, CE
S14, 5 A Nominal, Linear to 20 Amp, CE
S15, 1 A Nominal, Linear to 4 Amp, CE
S16, Dual Range, 5 Amp Nominal, Linear to 20 Amp/100 Amp, UL/CSA
S17, Dual Range, 1 A Nominal, Linear to 4 Amp/20 Amp, UL/CSA
S16, Dual Range, 5 Amp Nominal, Linear to 20 Amp/100 Amp, CE
S17, Dual Range, 1 A Nominal, Linear to 4 Amp/20 Amp, CE
S2C, 5A nominal, external split-core CTs (6), linear to 100A - No UL, CE & CSA
S2C, 5A nominal, external split-core CTs (6), linear to 100A, 50 FT - No UL, CE & CSA

Firmware Level

X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 11

8
9
A
H
B
C
D
J
E
F
G
K

Position 12

1

Position 13

Y

Position 14

1
2
3
4
5
6
7
8
9
A
R
P

Position 15

C

M87x Family Order Guides

Model: **M872**

M872, Modular IED in Large Chassis

Category and Description of Choices (X is Default)

Base Model

M872, IED, Modular, Dual Line, Dual Bus, C12A8 Chassis

Bay1, PowerSupply

V10, 20-300 V dc/55-285 V ac, plus blank slot cover
V10NC, 20-300 V dc/55-285 V ac, cover by P31 next Slot

Bay2, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
None, covered by P31 in next slot

Bay3, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
None, covered by P31 in next slot

Bay4, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
None, covered by P31 in next slot

Bay5, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
None, covered by P31 in next slot

Build Order#

Position 1-3

872

Position 4

3
4

Position 5

0
6
7
8
9
A
U
V
W
Y

Position 6

0
6
7
8
9
A
U
V
W
Y

Position 7

0
6
7
8
9
A
U
V
W
Y

Position 8

0
6
7
8
9
A
U
V
W
Y

Order Guide continues on next page

M87x Family Order Guides

Model: **M872**

M872, Modular IED in Large Chassis

Category and Description of Choices (X is Default)

Bay6, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
P40, Transducer Module, 8 inputs set for 0-10 V dc
P40, Transducer Module, 8 inputs set for 4-20 mA
P40, Transducer Module, 8 inputs set for 0-1 mA
None, covered by P31 in next slot

Bay7, cPCI Slot

C00, Blank Cover
P30A, Digital I/O, 8 point set for 100 V dc
P31, Digital I/O, 16 point set for 100 V dc
P30A, Digital I/O, 8 point set for 300 V dc
P31, Digital I/O, 16 point set for 300 V dc
P33, Eight Digital Outputs
Open for H12 with Ethernet option selected in next slot

Bay8, cPCI, Slot Host

H12M256, Host Processor, 256 MB
H12M512, Host Processor, 512 MB
H12M1GB, Host Processor, 1 GB
H12M2GB, Host Processor, 2 GB
H12M256E1, Host Processor, 256 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M512E1, Host Processor, 512 MB, with E1 Ethernet (RJ45 10/100Mb)
H12M1GBE1, Host Processor, 1 GB, with E1 Ethernet (RJ45 10/100Mb)
H12M2GBE1, Host Processor, 2 GB, with E1 Ethernet (RJ45 10/100Mb)
H12M256E3, Host Processor, 256 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M512E3, Host Processor, 512 MB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M1GBE3, Host Processor, 1 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)
H12M2GBE3, Host Processor, 2 GB, with E3 Ethernet (RJ45 10/100Mb + 100 Fiber)

Bay9, cPCI Slot, DSP

A10, Analog Processor

Bay10

None, Bay is covered by adjacent Signal Input Module

Bay11, Signal Input

S13, 5 A Nominal, Linear to 100 Amp, UL/CSA
S14, 5 A Nominal, Linear to 20 Amp, UL/CSA
S15, 1 A Nominal, Linear to 4 Amp, UL/CSA
S13, 5 A Nominal, Linear to 100 Amp, CE
S14, 5 A Nominal, Linear to 20 Amp, CE
S15, 1 A Nominal, Linear to 4 Amp, CE
S16, Dual Range, 5 Amp Nominal, Linear to 20 Amp/100 Amp, UL/CSA
S17, Dual Range, 1 A Nominal, Linear to 4 Amp/20 Amp, UL/CSA
S16, Dual Range, 5 Amp Nominal, Linear to 20 Amp/100 Amp, CE
S17, Dual Range, 1 A Nominal, Linear to 4 Amp/20 Amp, CE
S2C, 5A nominal, external split-core CTs (6), linear to 100A - No UL, CE & CSA
S2C, 5A nominal, external split-core CTs (6), linear to 100A, 50 FT - No UL, CE & CSA

Firmware Level

X, Latest Firmware Version

[End of Order Guide](#)

Build Order#

Position 9

0
6
7
8
9
A
U
V
W
Y

Position 10

0
6
7
8
9
A
Y

Position 11

8
9
A
H
B
C
D
J
E
F
G
K

Position 12

1

Position 13

Y

Position 14

1
2
3
4
5
6
7
8
9
A
R
P

Position 15

C